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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s): James B. Burr and William Schnaitter

Serial No.: Group Art Unit:

Filed: 10/10/03 Examiner:

Title: METHOD AND APPARATUS FOR OPTIMIZING BODY BIAS CONNECTIONS IN CMOS CIRCUITS USING A DEEP N-WELL GRID STRUCTURE

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
SW	A	6,489,224	12/03/02	Burr	438	526	05/31/01
SW	B	6,091,283	07/18/00	Murgula, et al.	327	537	02/24/98
SW	C	6,087,892	07/11/00	Burr	327	534	06/08/98
SW	D	6,048,746	04/11/00	Burr	438	17	06/08/98
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	G						
	H						
	I						
	J						
	K						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	Translation No
	L							
	M							
	N							
	O							
	P							
	Q							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	
	S	
	T	

Examiner	/Stacy Whitmore/	Date Considered	05/02/2006

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.